



# OpenFabrics Alliance

## Interoperability Logo Group (OFILG)

### Dec 2011 Logo Event Report

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 Report Revision: 1.2  
 OFED Version on Compute Nodes: 1.5.4  
 Operating System on Compute Nodes: SL 6.1

Enclosed are the results from OFA Logo testing performed on the following devices under test (DUTs):  
*QLogic 12200*

The test suite referenced in this report is available at the IOL website. Release 1.4 (2011-Oct-25) was used.  
[http://www.iol.unh.edu/services/testing/ofa/testsuites/OFA-IWG\\_Interoperability\\_Test\\_Plan-v1.40.pdf](http://www.iol.unh.edu/services/testing/ofa/testsuites/OFA-IWG_Interoperability_Test_Plan-v1.40.pdf)

The logo document referenced in this report is available at the IOL website. Release 1.14 (2011-Mar-01) was used.  
[http://www.iol.unh.edu/services/testing/ofa/logoprogram/OFA-UNH-IOL\\_Logo\\_Program-v1.14.pdf](http://www.iol.unh.edu/services/testing/ofa/logoprogram/OFA-UNH-IOL_Logo_Program-v1.14.pdf)

The Following Table highlights the Mandatory test results required for the OpenFabrics Interoperability Logo for the DUT per the Test Plan referenced above and the current OpenFabrics Interoperability Logo Program (OFILP).

Additional beta testing was performed using the DUT than is reflected in this report. A separate report will outline those results.

Test Procedures	IWG Test Status	Result/Notes
<a href="#">10.1: Link Initialization</a>	Mandatory	<b>PASS with Comments</b>
<a href="#">10.2: IB Fabric Initialization</a>	Mandatory	PASS
<a href="#">10.3: IPoIB Connected Mode</a>	Mandatory	PASS
<a href="#">10.4: IPoIB Datagram Mode</a>	Mandatory	PASS
<a href="#">10.5: SM Failover and Handover</a>	Mandatory	PASS
<a href="#">10.6: SRP</a>	Mandatory	PASS
<a href="#">12.1: TI iSER</a>	Mandatory	Not Available
<a href="#">12.2: TI NFS over RDMA</a>	Mandatory	PASS
<a href="#">12.3: TI RDS</a>	Mandatory	PASS
<a href="#">12.4: TI SDP</a>	Mandatory	PASS
<a href="#">12.5: TI uDAPL</a>	Mandatory	PASS
<a href="#">12.6: TI RDMA Basic Interop</a>	Mandatory	PASS
<a href="#">12.8: TI RDMA Stress</a>	Mandatory	PASS
<a href="#">12.11: TI MPI – Open</a>	Mandatory	PASS
<a href="#">12.12: TI MPI – OSU</a>	Mandatory	PASS

Summary of all results follows on the second page of this report.  
 For Specific details regarding issues, please see the corresponding test result.

Testing Completed 05 January 2012

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Review Completed 16 March 2012

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## Result Summary

The Following table summarizes all results from the event pertinent to this IB device class.

Test Procedures	IWG Test Status	Result/Notes
<a href="#">10.1: Link Initialization</a>	Mandatory	PASS with Comments
<a href="#">10.2: IB Fabric Initialization</a>	Mandatory	PASS
<a href="#">10.3: IPoIB Connected Mode</a>	Mandatory	PASS
<a href="#">10.4: IPoIB Datagram Mode</a>	Mandatory	PASS
<a href="#">10.5: SM Failover and Handover</a>	Mandatory	PASS
<a href="#">10.6: SRP</a>	Mandatory	PASS
<a href="#">10.7: Ethernet Gateway</a>	Beta	Not Tested
<a href="#">10.8: FibreChannel Gateway</a>	Beta	Not Tested
<a href="#">12.1: TI iSER</a>	Mandatory	Not Available
<a href="#">12.2: TI NFS over RDMA</a>	Mandatory	PASS
<a href="#">12.3: TI RDS</a>	Mandatory	PASS
<a href="#">12.4: TI SDP</a>	Mandatory	PASS
<a href="#">12.5: TI uDAPL</a>	Mandatory	PASS
<a href="#">12.6: TI RDMA Basic Interoperability</a>	Mandatory	PASS
<a href="#">12.8: TI RDMA Stress</a>	Mandatory	PASS
<a href="#">12.10: TI MPI – Intel</a>	Beta	Not Tested
<a href="#">12.11: TI MPI – Open</a>	Mandatory	PASS
<a href="#">12.12: TI MPI – OSU</a>	Mandatory	PASS

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## Report Revision History

- v1.0 Initial working copy
- v1.1 Revised working copy
- v1.2 Post arbitration resolution update

## Configuration Files

Description	Attachment
Scientific Linux 6.1 Configuration File	
OFED 1.5.4 Configuration File	

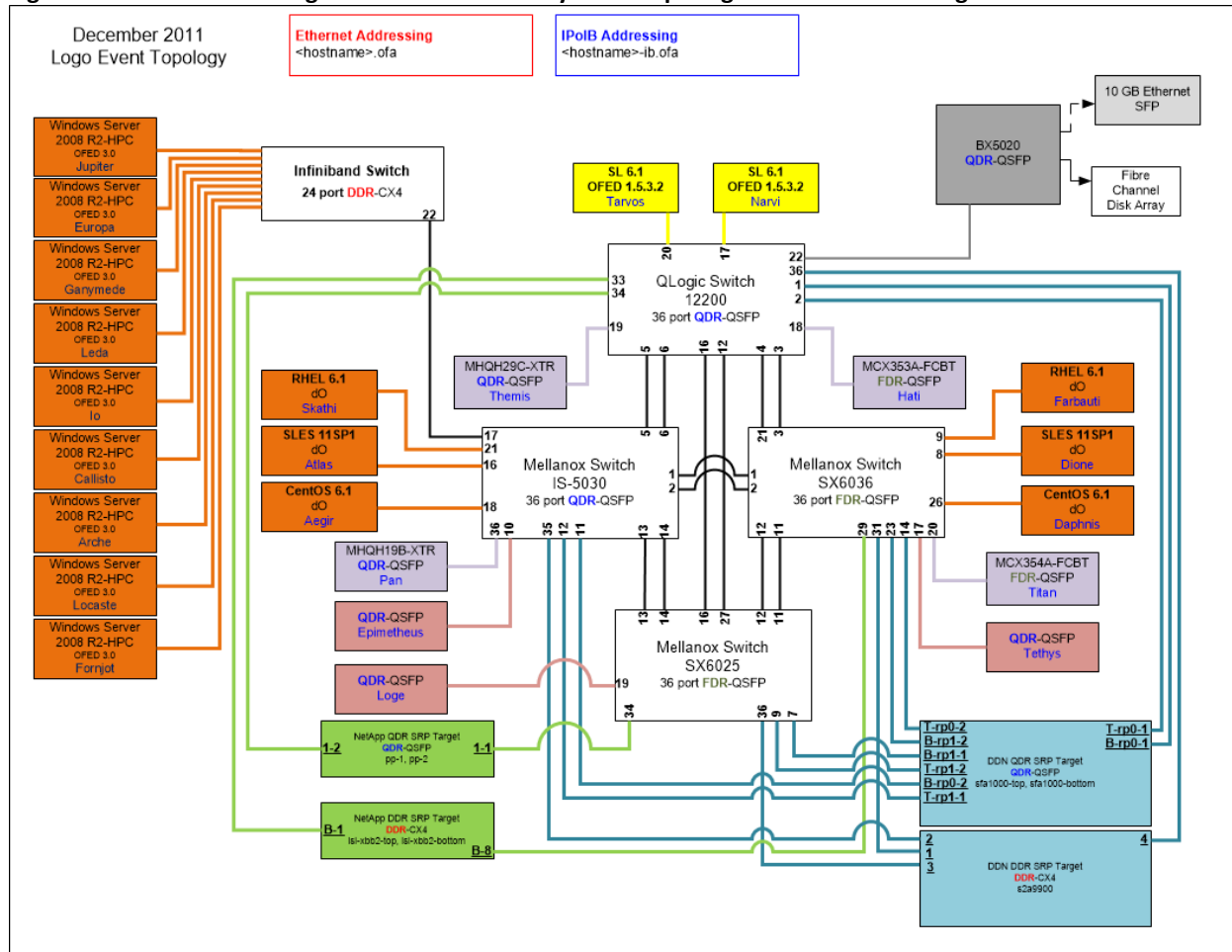
## Result Key

The following table contains possible results and their meanings:

Result:	Description:
<b>PASS</b>	The Device Under Test (DUT) was observed to exhibit conformant behavior.
<b>PASS with Comments</b>	The DUT was observed to exhibit conformant behavior however an additional explanation of the situation is included.
<b>FAIL</b>	The DUT was observed to exhibit non-conformant behavior.
<b>Warning</b>	The DUT was observed to exhibit behavior that is not recommended.
<b>Informative</b>	Results are for informative purposes only and are not judged on a pass or fail basis.
<b>Refer to Comments</b>	From the observations, a valid pass or fail could not be determined. An additional explanation of the situation is included.
<b>Not Applicable</b>	The DUT does not support the technology required to perform this test.
<b>Not Available</b>	Due to testing station limitations or time limitations, the tests could not be performed.
<b>Borderline</b>	The observed values of the specific parameters are valid at one extreme and invalid at the other.
<b>Not Tested</b>	Not tested due to the time constraints of the test period.

# DUT and Test Setup Information

Figure 1: The IB fabric configuration utilized for any tests requiring a multi-switch configuration is shown below.



DUT #1 Details			
Manufacturer:	QLogic	Firmware Revision:	7.0.1.0.29
Model:	12200	Hardware Revision:	17
Speed:	QDR	Located in Host:	NA
Firmware MD5sum:	ee828cf5e7a37ca52da42515a32a3dd7		
Additional Comments / Notes:			

## Mandatory Tests – IB Device Test Results:

### 10.1: Link Initialization

Results	
Part #1:	Refer to Comments
Discussion:	
It was noted during testing that certain port combinations between the QLogic 12200 switch and the Mellanox SX6036 switch linked at 4X DDR instead of the expected 4x QDR. This was more readily apparent when using passive copper cables five (5) meters in length when connected between ports on with long traces (generally edge ports).	

Link Partner	12200
QLogic 12200 (Switch) – QDR	NA
Mellanox SX6025 (Switch) – FDR	PASS
Mellanox SX6036 (Switch) – FDR	PASS with Comments
Mellanox IS-5030 (Switch) – QDR	PASS
Mellanox BX5020 (Gateway) – QDR	PASS
DataDirect Networks SFA10000 (SRP Target) – QDR	PASS
DataDirect Networks S2A9900 (SRP Target) – DDR	PASS
NetApp Pikes Peak (SRP Target) – QDR	PASS
NetApp XBB2 (SRP Target) – DDR	PASS
Host: Themis	HCA: MHQH29C-XTR (QDR) PASS
Host: Pan	HCA: MHQH19B-XTR (QDR) PASS
Host: Hati	HCA: MCX353A-FCBT (FDR) PASS
Host: Titan	HCA: MCX354A-FCBT (FDR) PASS

### 10.2: Fabric Initialization

Subnet Manager				
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
PASS	PASS	PASS	PASS	PASS
Result Discussion:				
All subnet managers used while testing with OFED 1.5.4 were able to correctly configure the selected topology.				

### 10.3: IPoIB Connected Mode

Subnet Manager					
Part	OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
A – ping	PASS	PASS	PASS	PASS	PASS
B – SFTP	PASS	PASS	PASS	PASS	PASS
C – SCP	PASS	PASS	PASS	PASS	PASS
Result Discussion:					
IPoIB ping, SFTP, and SCP transactions completed successfully between all HCAs; each HCA acted as both a client and a server for all tests.					

**10.4: IPoB Datagram Mode**

Subnet Manager					
Part	OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
A – ping	PASS	PASS	PASS	PASS	PASS
B – SFTP	PASS	PASS	PASS	PASS	PASS
C – SCP	PASS	PASS	PASS	PASS	PASS
<b>Result Discussion:</b>					
IPoB ping, SFTP, and SCP transactions completed successfully between all HCAs; each HCA acted as both a client and a server for all tests.					

**10.5: SM Failover and Handover**

SM Pairings		Result
OpenSM OFED 1.5.4	OpenSM OFED 1.5.4	PASS
<b>Result Discussion:</b>		
OpenSM was able to properly handle SM priority and state rules.		

**10.6: SRP**

Subnet Manager				
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
PASS	PASS	PASS	PASS	PASS
<b>Result Discussion:</b>				
SRP communications between all HCAs and all SRP targets succeeded while the above mentioned SMs were in control of the fabric.				

**12.1 TI iSER**

Subnet Manager				
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
Not Tested	Not Tested	Not Tested	Not Tested	Not Tested
<b>Result Discussion:</b>				
This test was not performed as there are no devices that support the iSER test procedure present in event topology.				

**12.2: TI NFS over RDMA**

Subnet Manager				
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
PASS	PASS	PASS	PASS	PASS
<b>Result Discussion:</b>				
Connectathon was used to test NFS over RDMA; each HCA acted as both a client and a server.				

### 12.3: TI RDS

Subnet Manager					
Part	OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
A – ping	PASS	PASS	PASS	PASS	PASS
B – stress	PASS	PASS	PASS	PASS	PASS
<b>Result Discussion:</b>					
The reliable datagram socket protocol was tested between all HCAs; all communications completed successfully.					

### 12.4: TI SDP

Subnet Manager					
Part	OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
A – netperf	PASS	PASS	PASS	PASS	PASS
B – SFTP	PASS	PASS	PASS	PASS	PASS
C – SCP	PASS	PASS	PASS	PASS	PASS
<b>Result Discussion:</b>					
All communications using the SDP protocol completed successfully; each HCA acted as both a client and a server for all tests.					

### 12.5: TI uDAPL

Subnet Manager					
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM	
PASS	PASS	PASS	PASS	PASS	
<b>Result Discussion:</b>					
All communications using DAPL were seen to complete successfully as described in the referenced test plan; each HCA acted as both a client and a server for all tests.					

### 12.6: TI RDMA Basic Interoperability

Subnet Manager					
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM	
PASS	PASS	PASS	PASS	PASS	
<b>Result Discussion:</b>					
All devices were shown to correctly exchange core RDMA operations across a simple network path under nominal (unstressed) conditions; each HCA acted as both a client and a server for all tests.					

### 12.8: TI RDMA Stress

Subnet Manager					
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM	
PASS	PASS	PASS	PASS	PASS	
<b>Result Discussion:</b>					
All IB switches were seen to properly handle a large load as indicated by the successfully completion of control communications between two HCAs while all other HCAs in the fabric were used to generate traffic in order to put a high load on the switch. Each HCA acted as both a client and a server for the control connection.					

**12.11: TI MPI – Open**

Subnet Manager					
Part	OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
A	PASS	PASS	PASS	PASS	PASS
B	PASS	PASS	PASS	PASS	PASS
<b>Result Discussion:</b>					
Complete heterogeneity; 1 MPI process per OFED 1.5.4 deployed system as described in the cluster topology (red and purple system icons), IB device vendor agnostic.					

**12.12: TI MPI – OSU**

Subnet Manager					
Part	OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
A	PASS	PASS	PASS	PASS	PASS
B	PASS	PASS	PASS	PASS	PASS
<b>Result Discussion:</b>					
Complete heterogeneity; 1 MPI process per OFED 1.5.4 deployed system as described in the cluster topology (red and purple system icons), IB device vendor agnostic.					



## Beta Tests – IB Device Test Results:

### 10.7: IB Ethernet Gateway

Subnet Manager				
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
Not Tested	Not Tested	Not Tested	Not Tested	Not Tested
<b>Result Discussion:</b>				
This test was not performed as there are no devices that support the Ethernet Gateway test procedure present in event topology.				

### 10.8 IB FibreChannel Gateway

Subnet Manager				
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
Not Tested	Not Tested	Not Tested	Not Tested	Not Tested
<b>Result Discussion:</b>				
This test was not performed as there are no devices that support the FibreChannel Gateway test procedure present in event topology.				

### 12.10: MPI – Intel

Subnet Manager				
OpenSM	IS-5030 SM	SX-6036 SM	QL12200 SM	WinOF SM
Not Tested	Not Tested	Not Tested	Not Tested	Not Tested
<b>Result Discussion:</b>				
This test was not performed as the binaries for Intel MPI are not present on the compute nodes present in event topology.				